

AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph starting at page 12, line 16, with the following replacement paragraph:

Figure 9 illustrates one embodiment of a generic hardware system intended to represent a broad category of computer systems such as personal computers, workstations, and/or embedded systems. In the illustrated embodiment, the hardware system includes processor 910 including cache 402 and error detect logic in accordance with embodiments of the invention. Processor 910 is coupled to high speed bus 905, which is coupled to input/output (I/O) bus 915 through bus bridge 930. Temporary memory 920 is coupled to bus 905. In various embodiments, temporary memory 920 may comprise dynamic random access memory (DRAM). Permanent memory 940 is coupled to bus 915. I/O device(s) 950 is also coupled to bus 915. I/O device(s) 950 may include a display device, a keyboard, one or more external network interfaces, etc.